L Number	Hits	Search Text	DB	Time stamp
1	1023	(memory adj controller) and (host adj processor)	USPAT; EPO; JPO; DERWENT; USOCR	2004/05/02 15:28
2	4	((memory adj controller) and (host adj processor)) and (parasitic adj capacitance)	USPAT; EPO; JPO; DERWENT; USOCR	2004/05/02 15:29
3	559	((memory adj controller) and (host adj processor)) and switch$3	USPAT; EPO; JPO; DERWENT; USOCR	2004/05/02 15:29
4	22	(((memory adj controller) and (host adj processor)) and switch$3) and capacitance	USPAT; EPO; JPO; DERWENT; USOCR	2004/05/02 15:29

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